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… a method for checking lock step consistency between an in circuit **emulation** and a microcontroller …

- all 3 versions »

C Nemecek - US Patent 6,922,821, 2005 - freepatentsonline.com

... of the FPGA implementation of an **emulator** for the **...** eg, sleep, **stall**, internal start/stop, **stalls**, etc.) a **...** However, using a **clock**, which is two times faster, a **...**

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